

FIG. 1

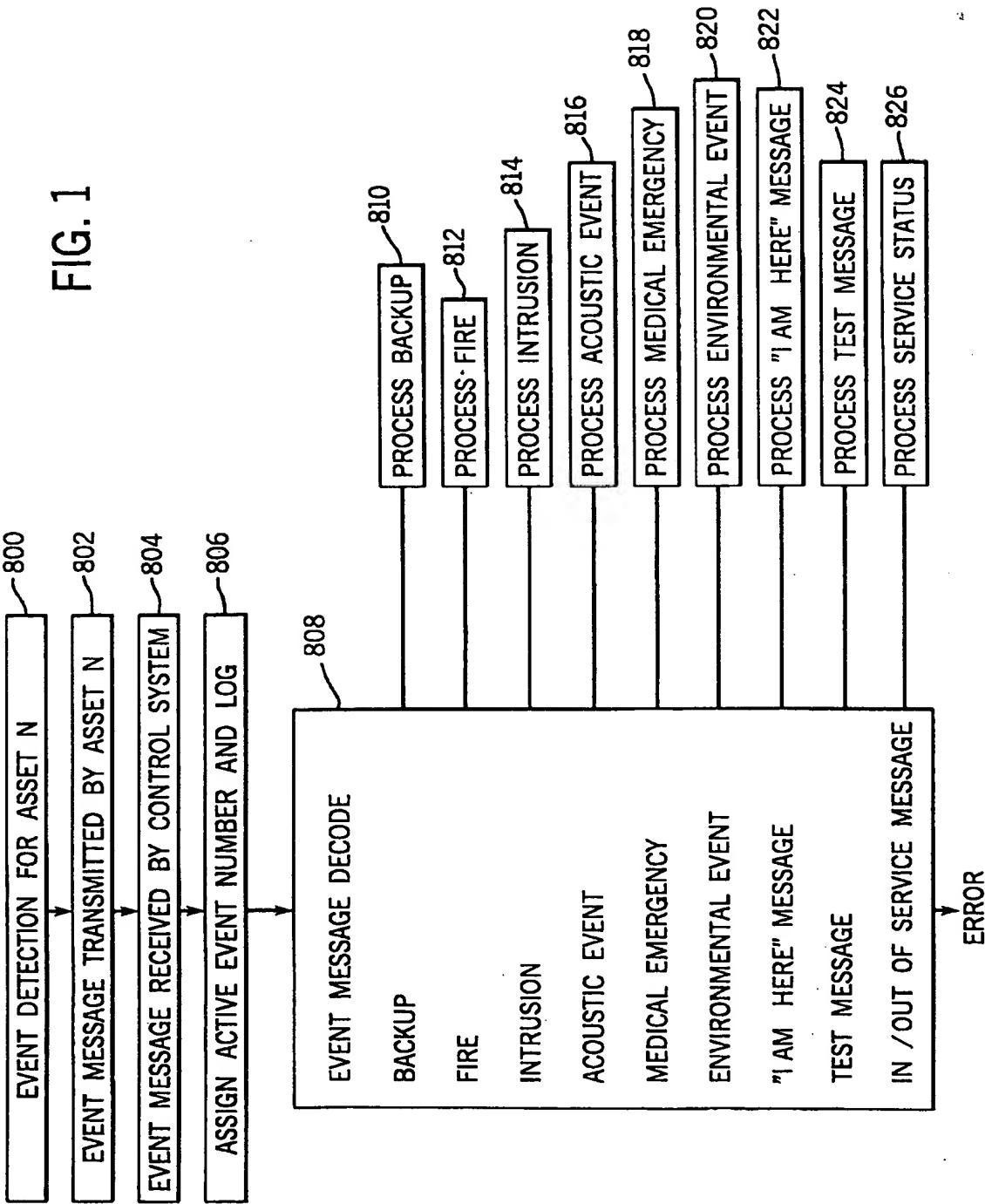


FIG. 2

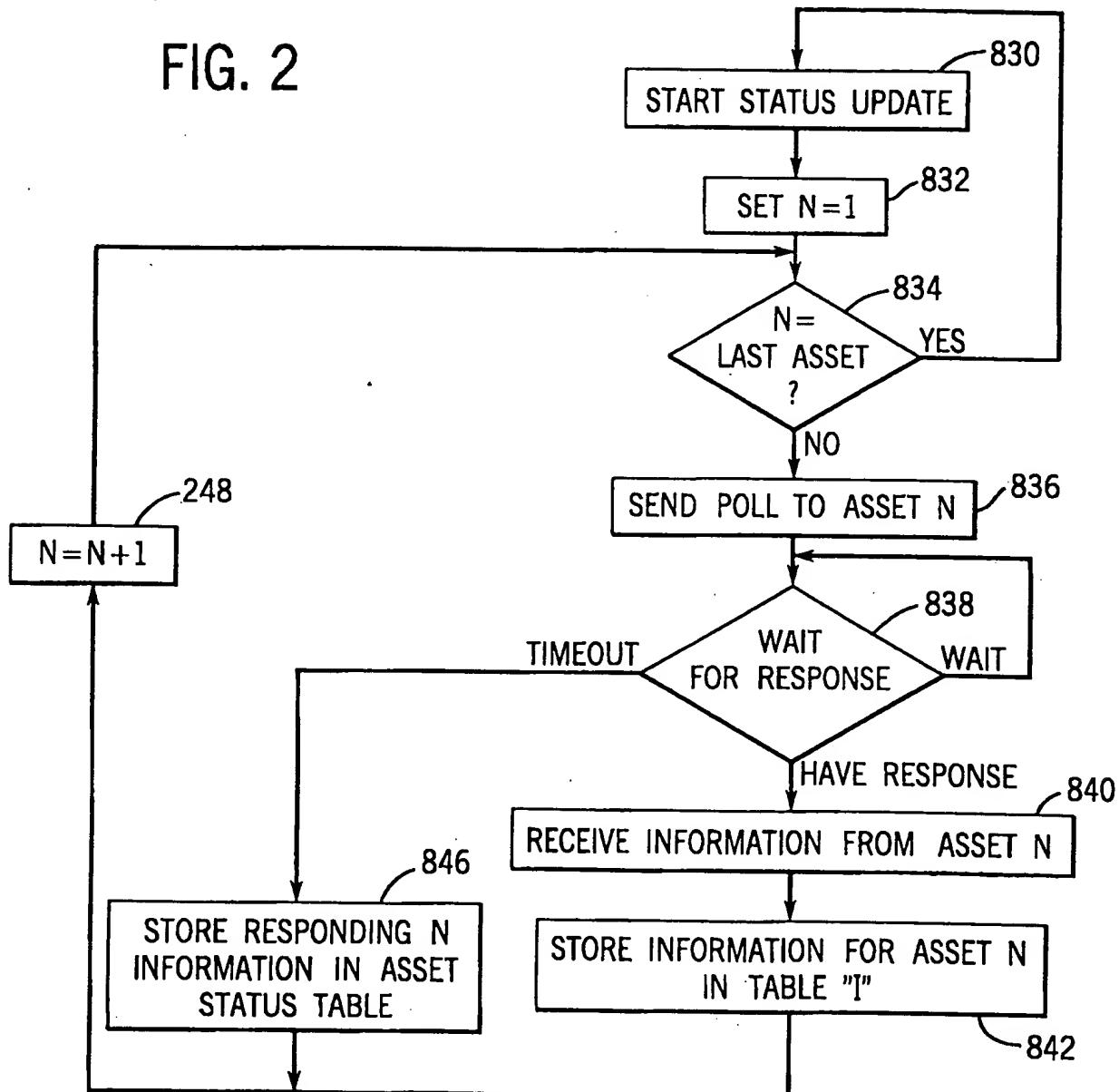


FIG. 3

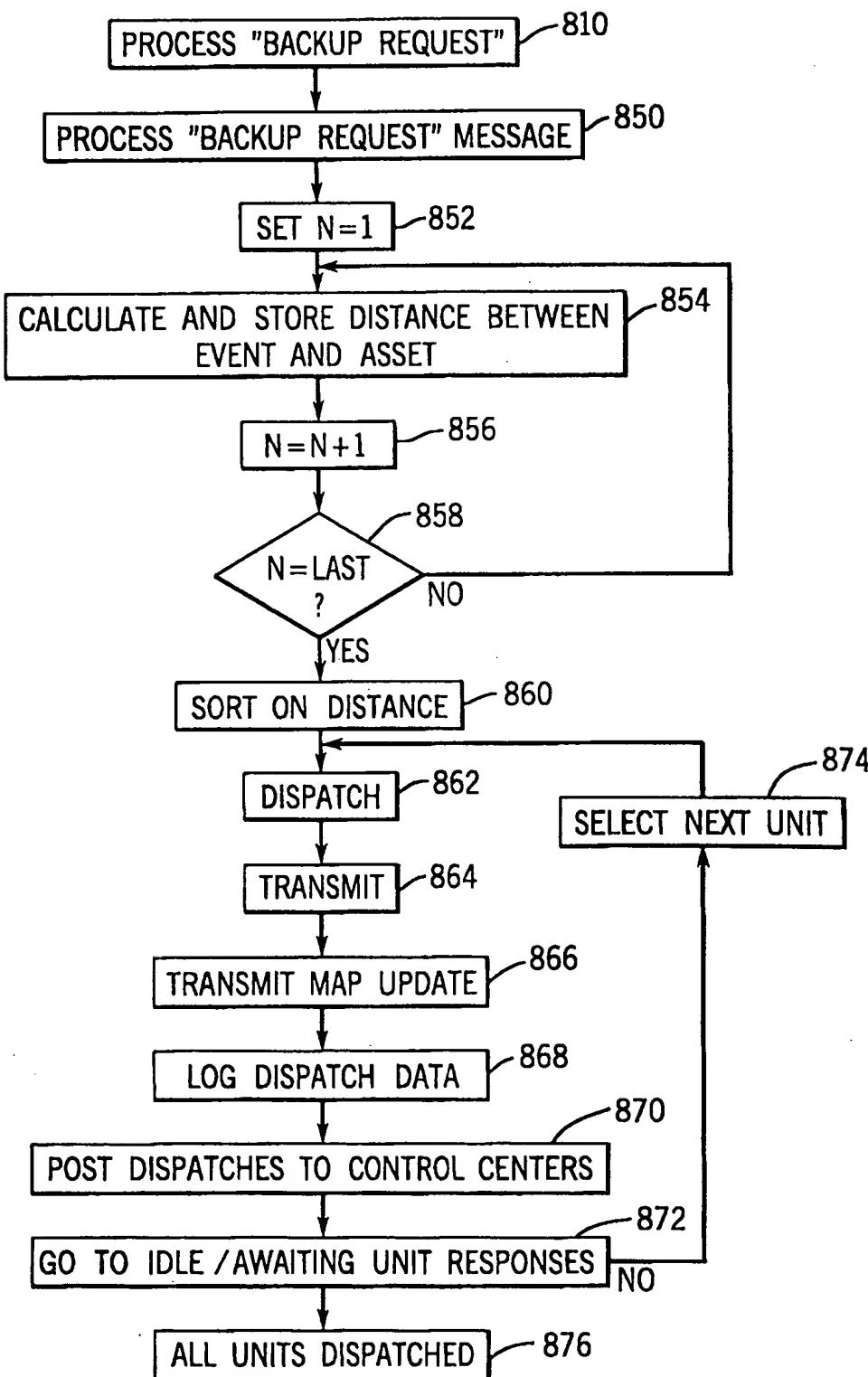
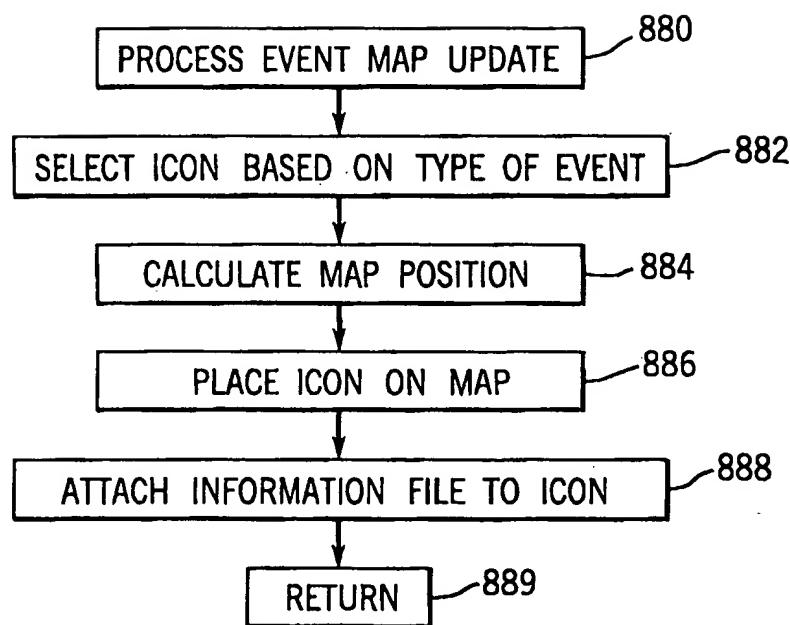


FIG. 4



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FIG. 5

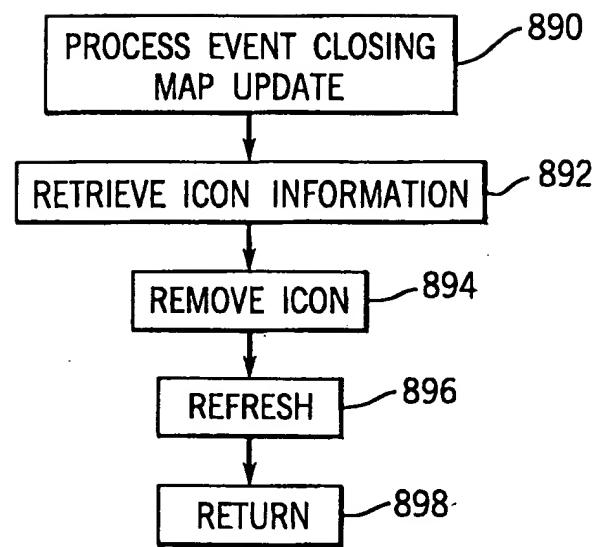
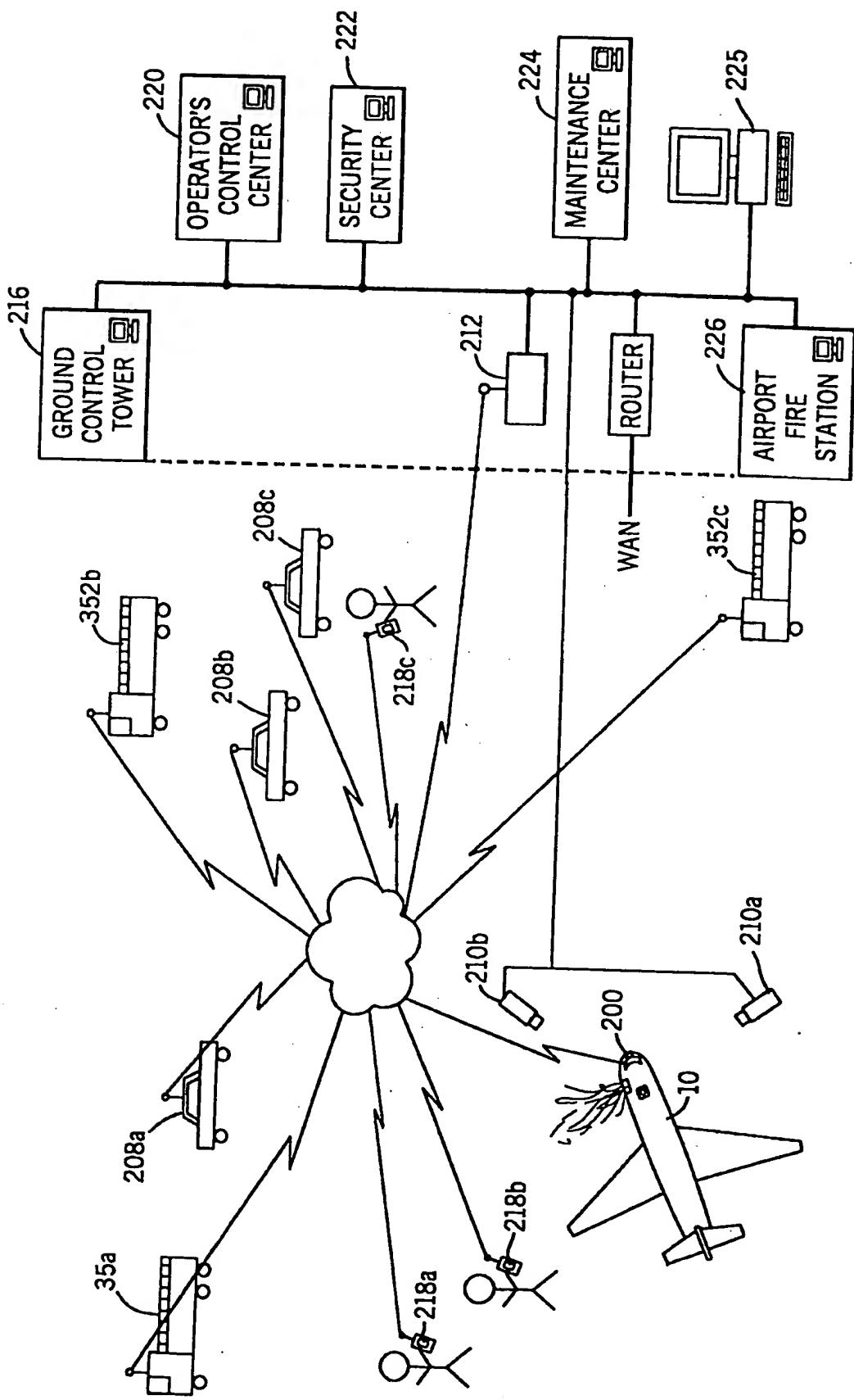
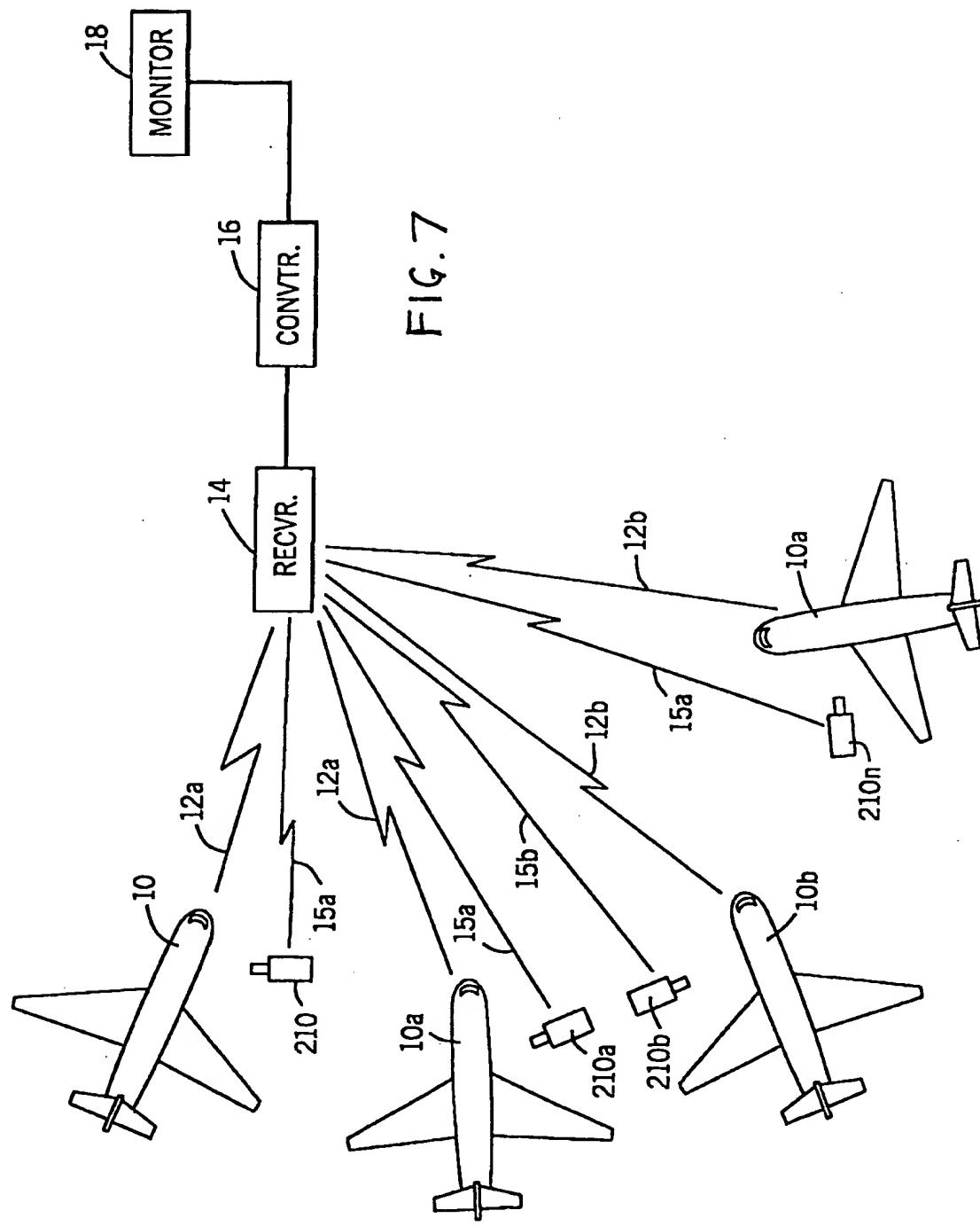


FIG. 6





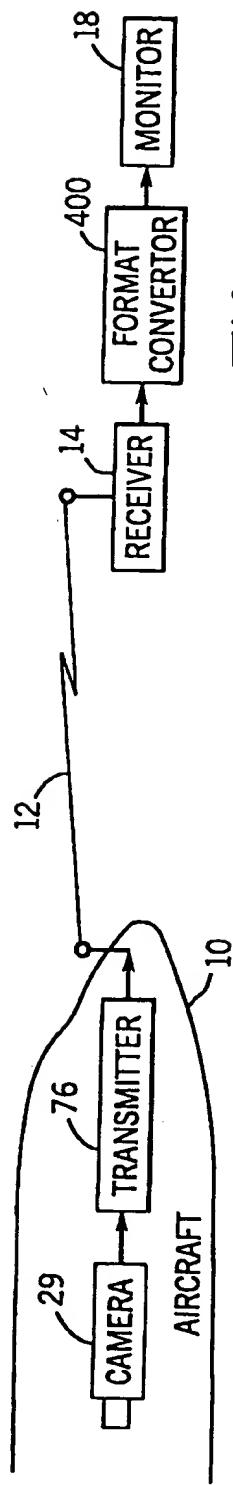


FIG. 8a

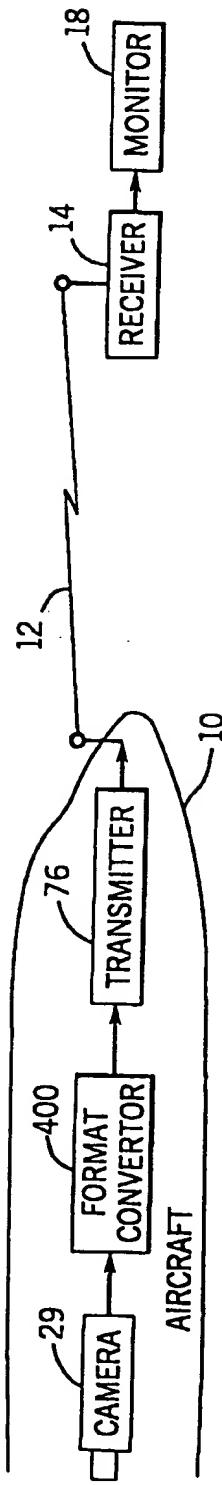


FIG. 8b

Fig. 9a

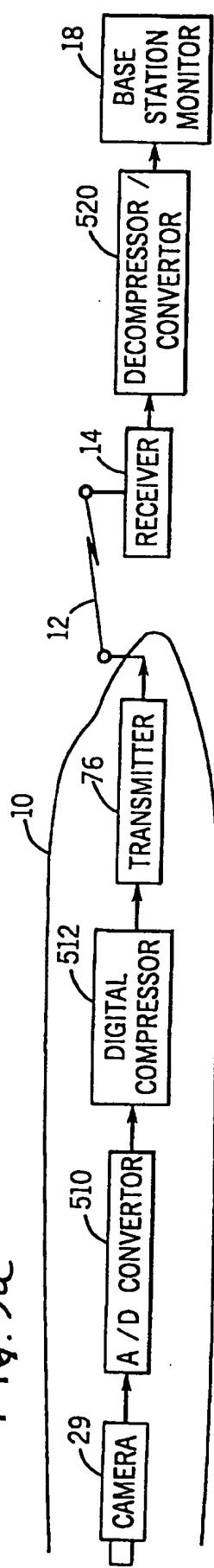


Fig. 9b

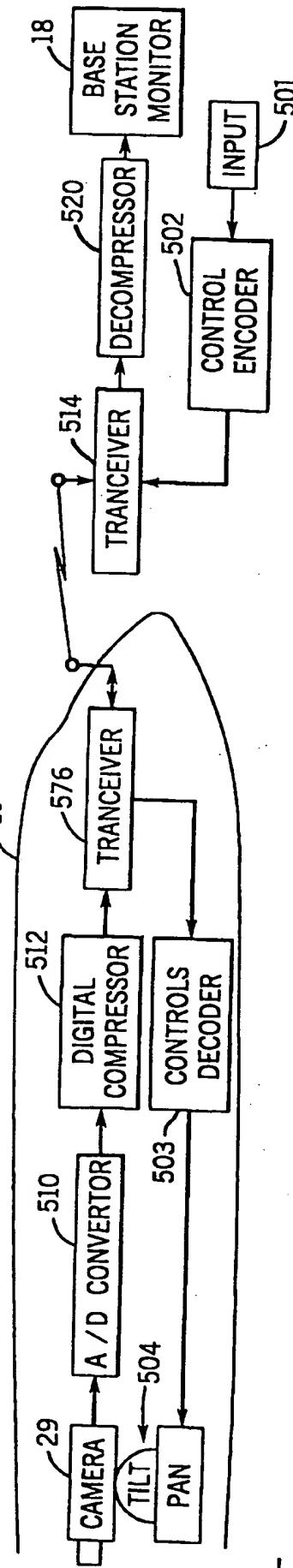


FIG. 10a

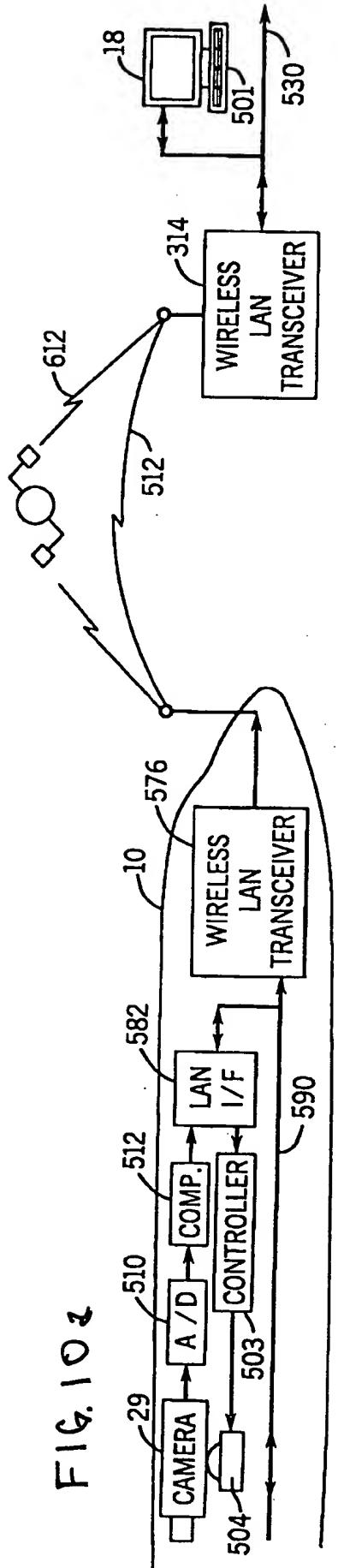
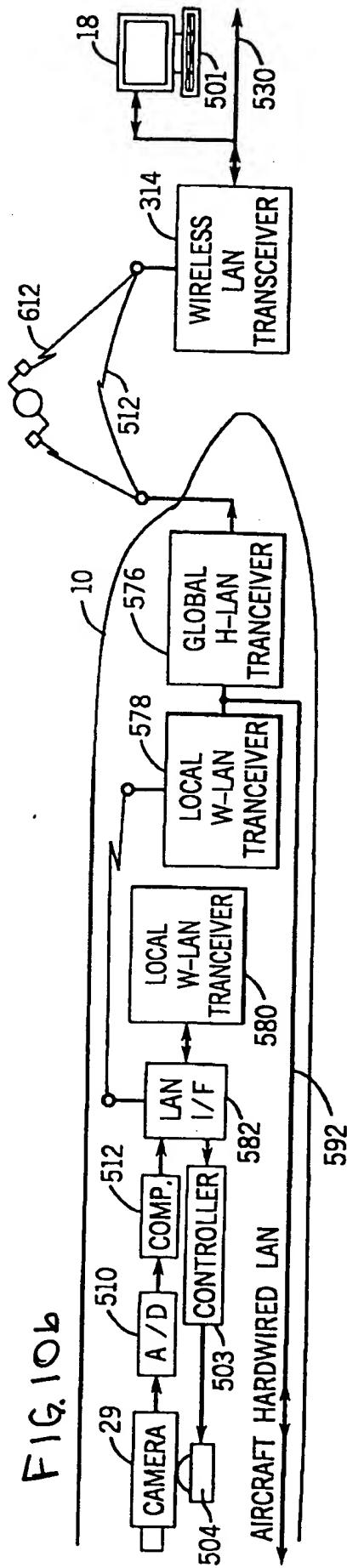
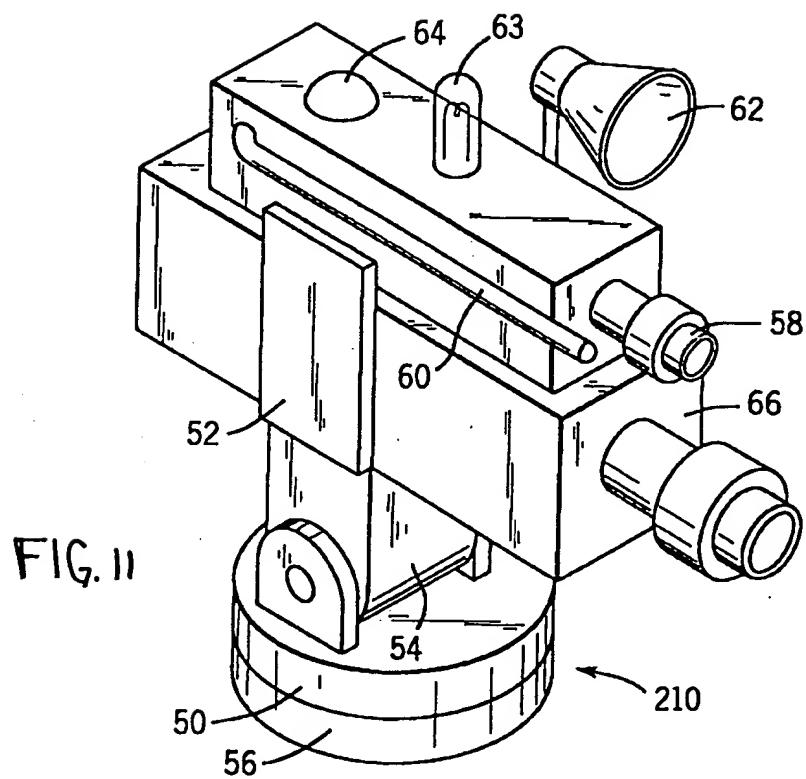


FIG. 10b





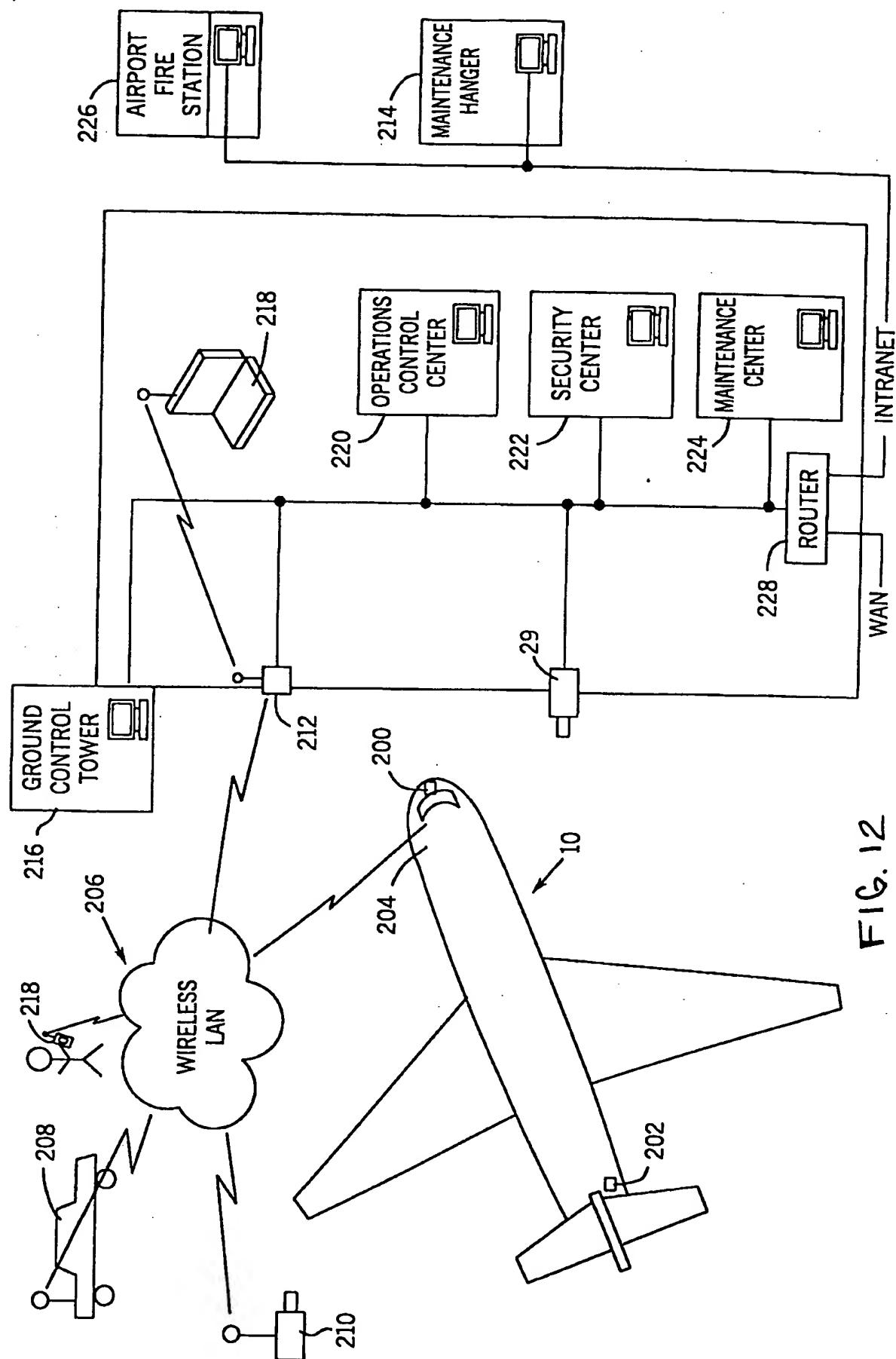


FIG. 12

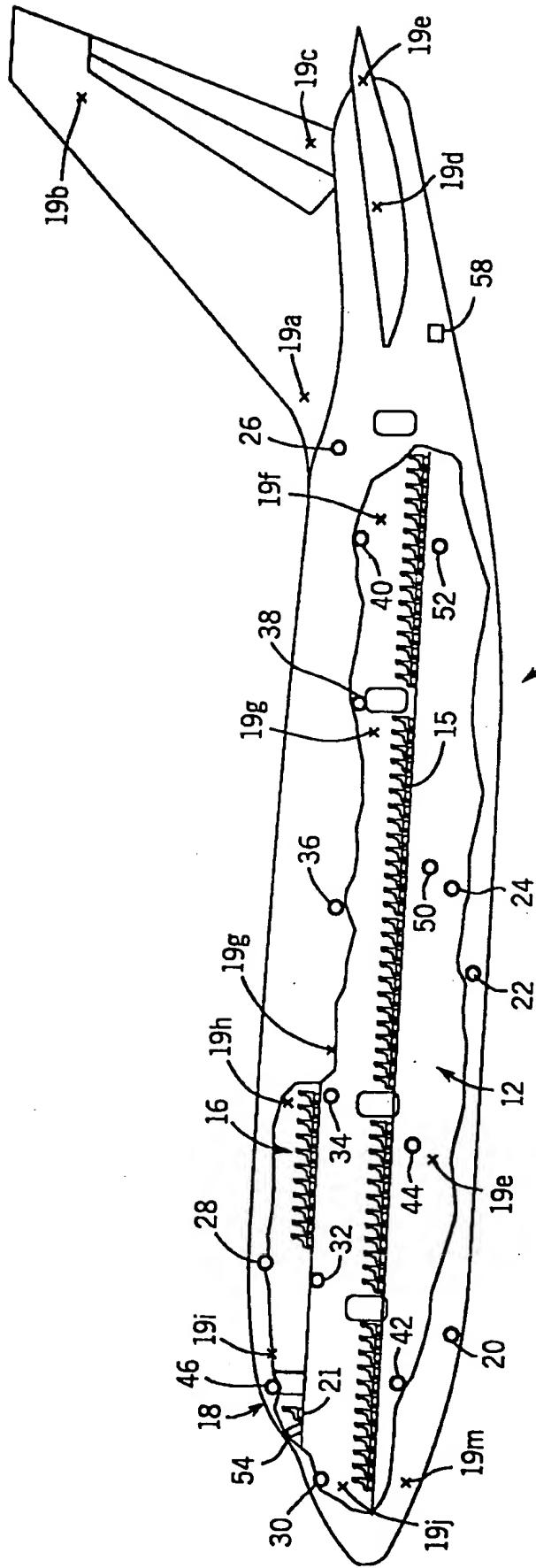


FIG. 13

FIG. 14

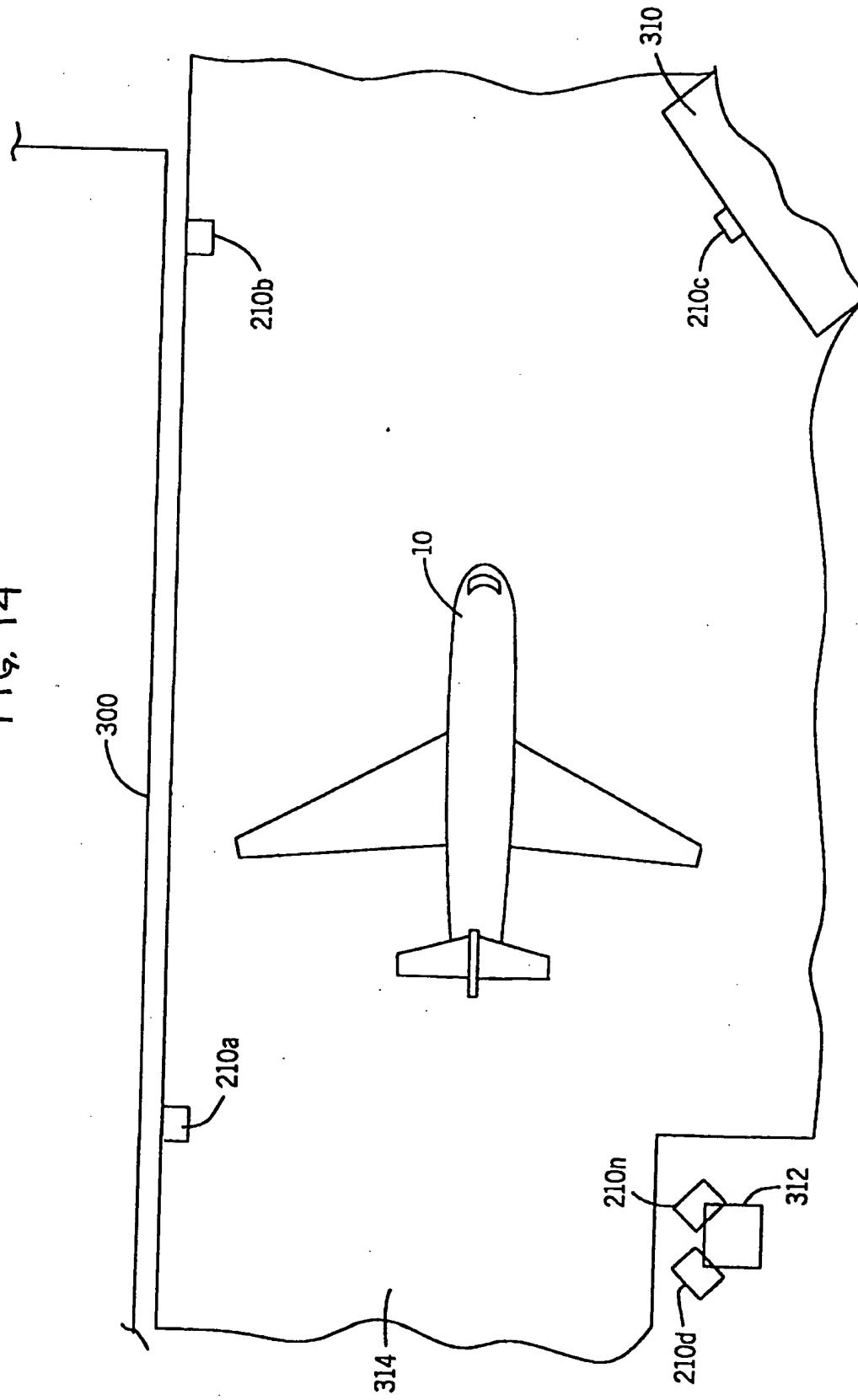
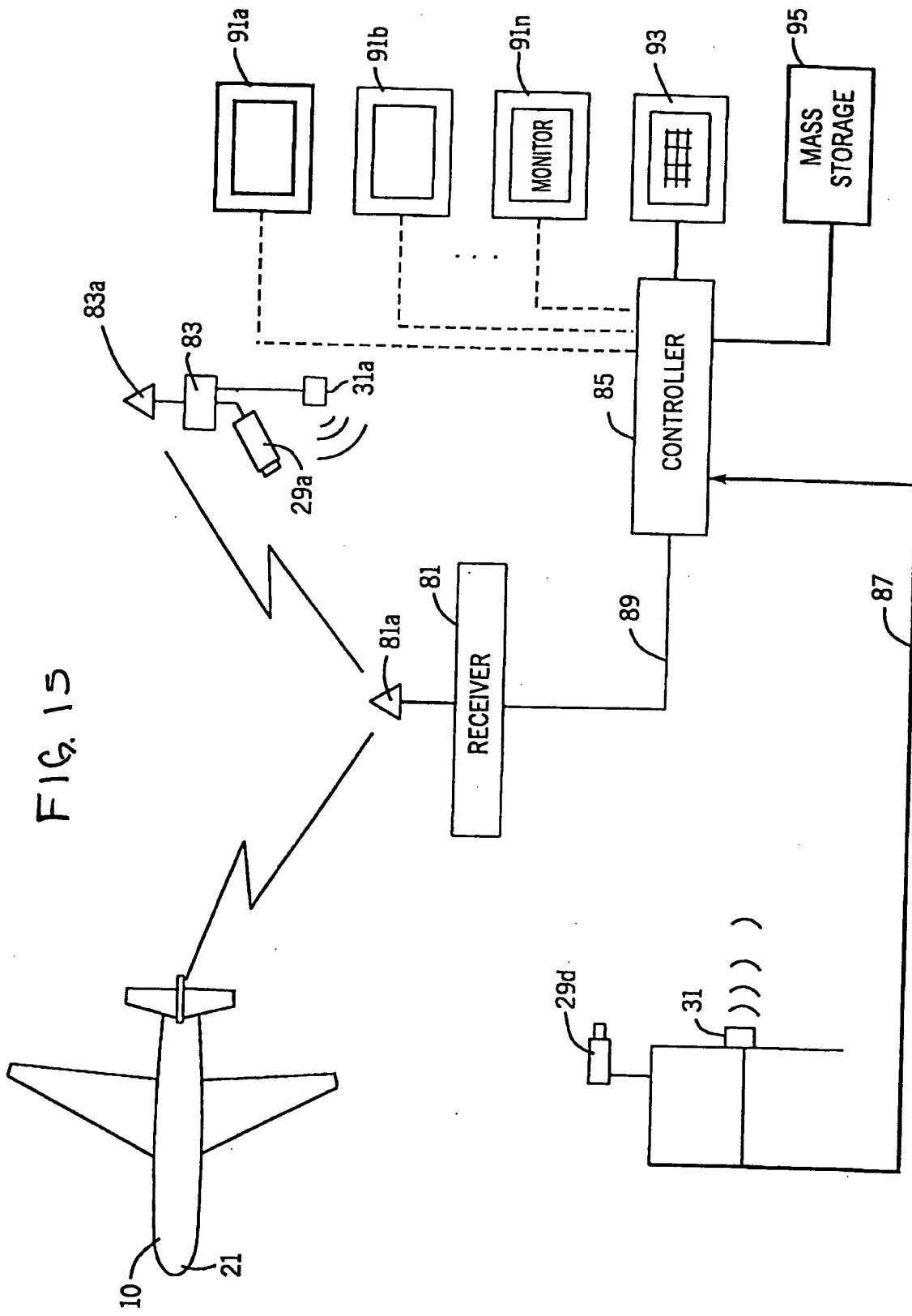


FIG. 15



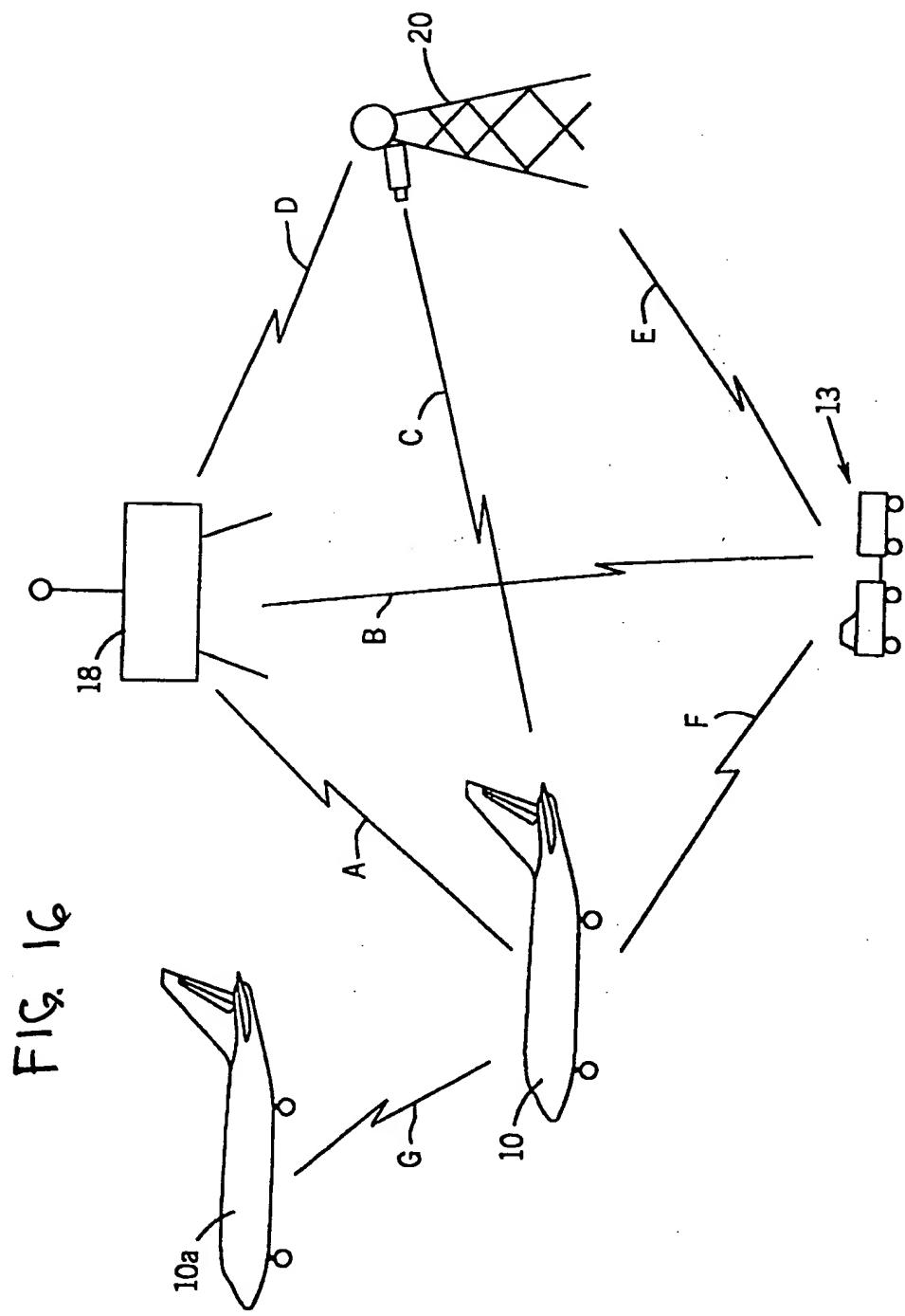


FIG. 16

FIG. 17

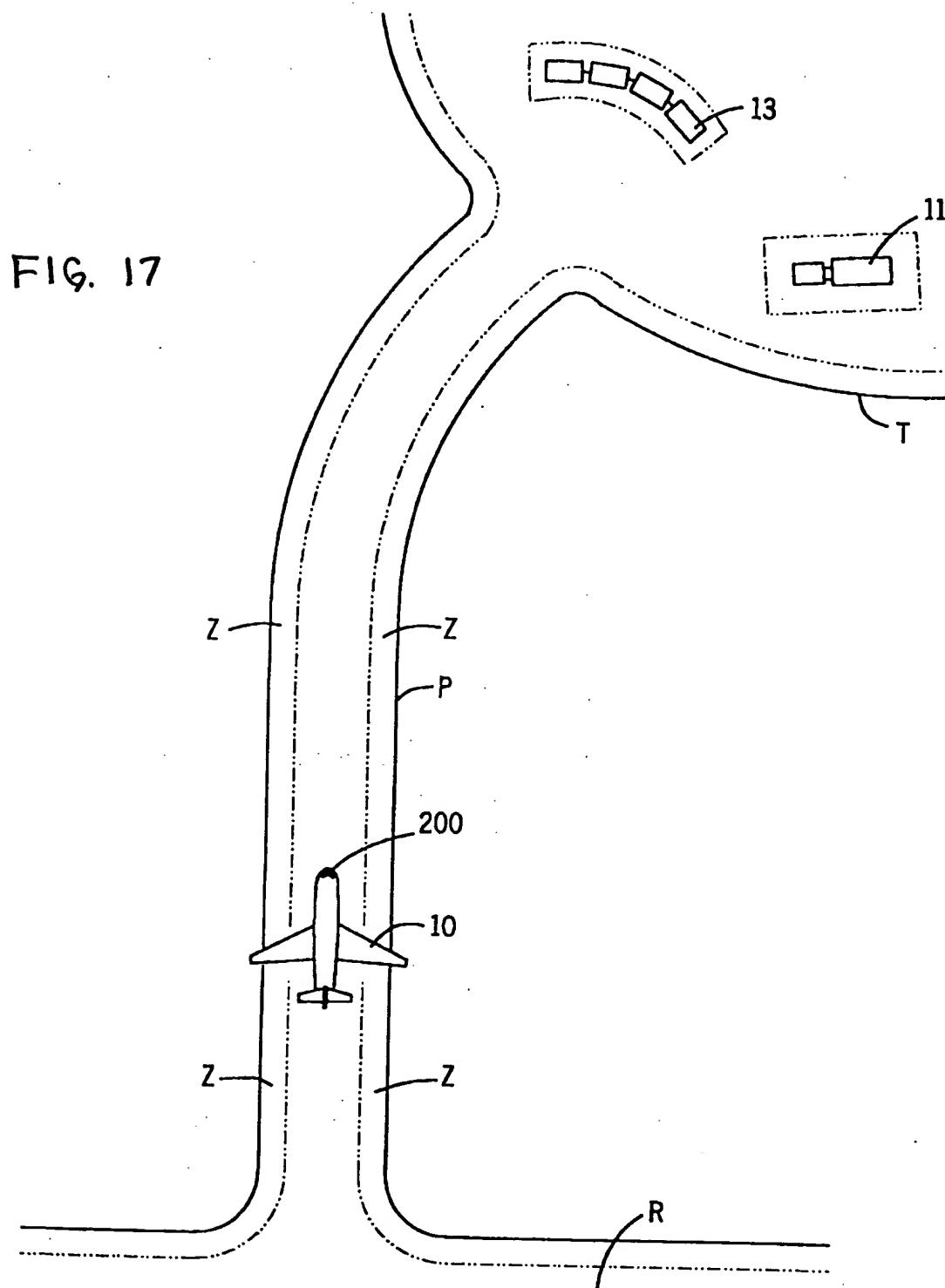
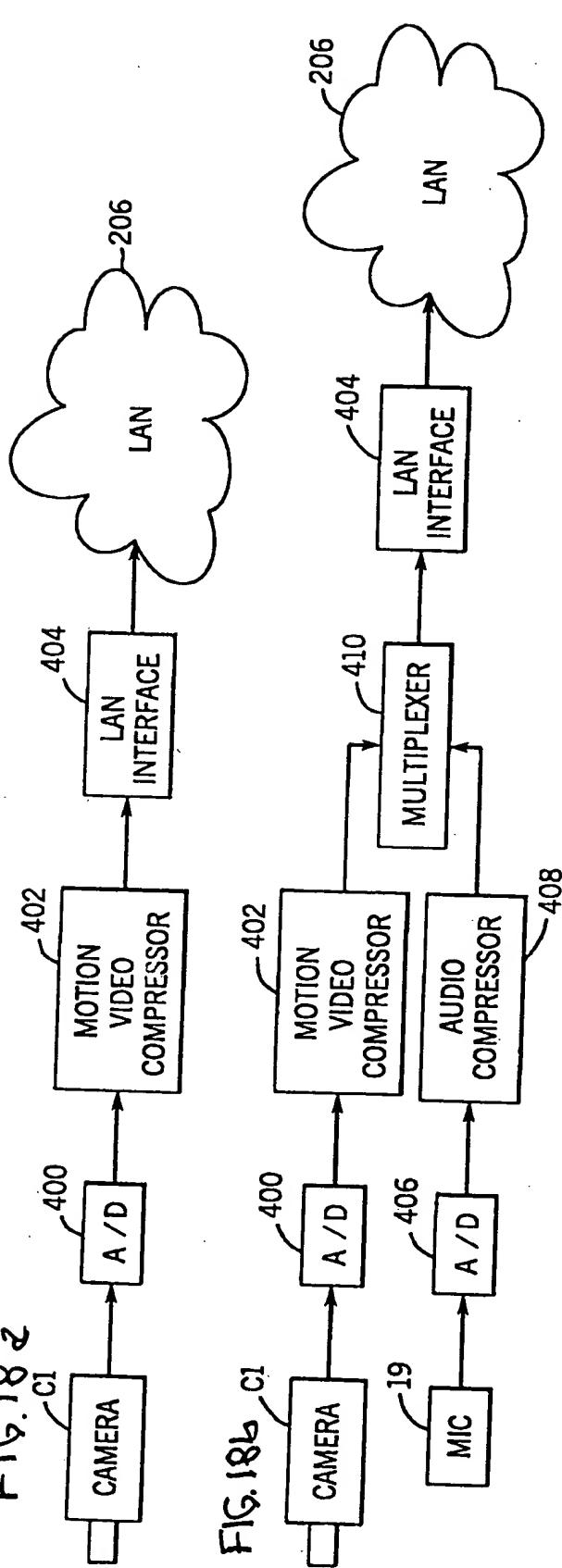


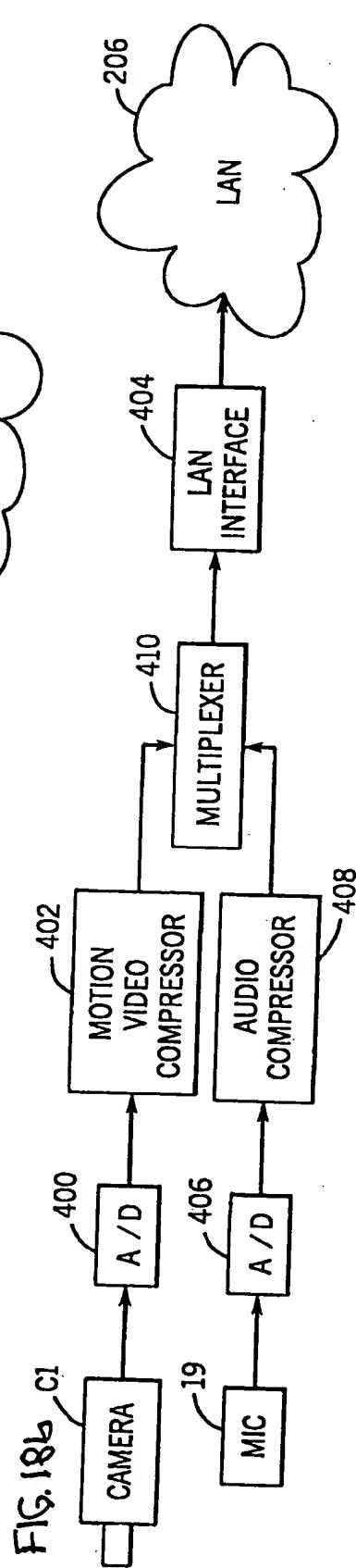
FIG. 18a



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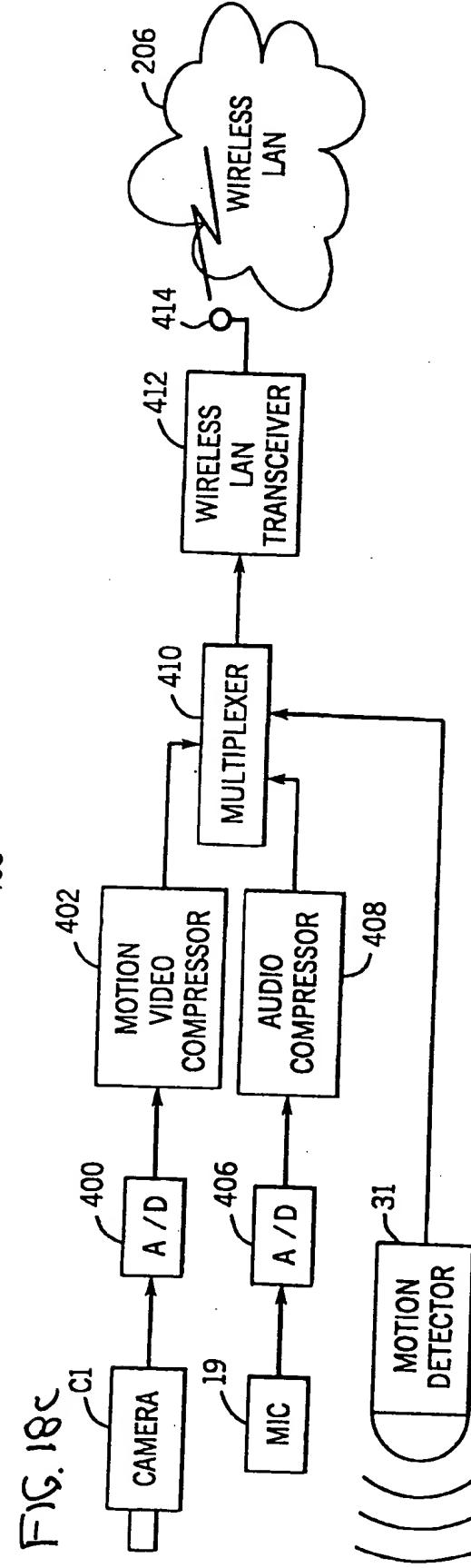
+

FIG. 18b



+

FIG. 18c



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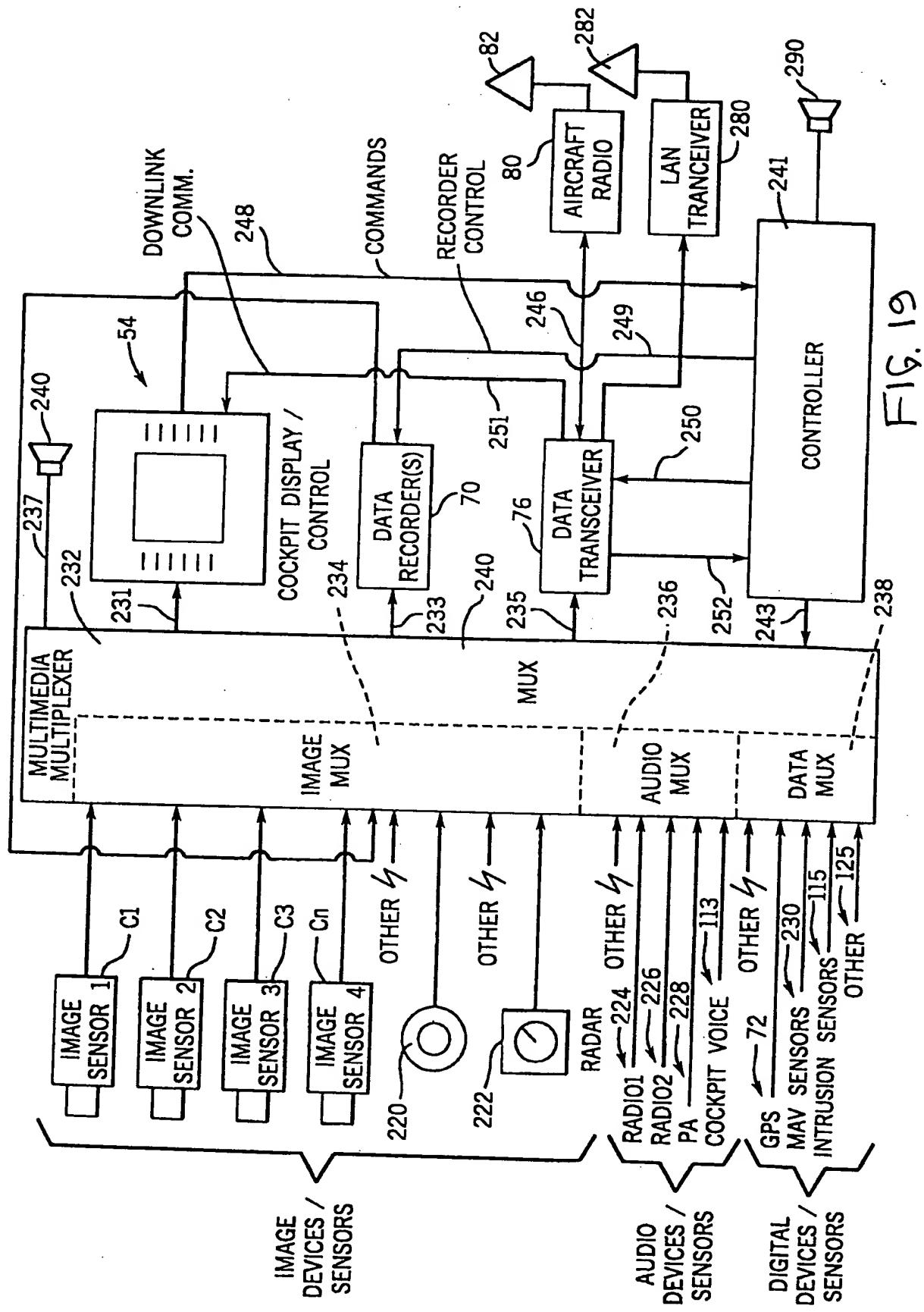


FIG. 19

FIG. 20

SENSOR I/D ADDRESS-
(SWITCHES OR DATA LOAD)

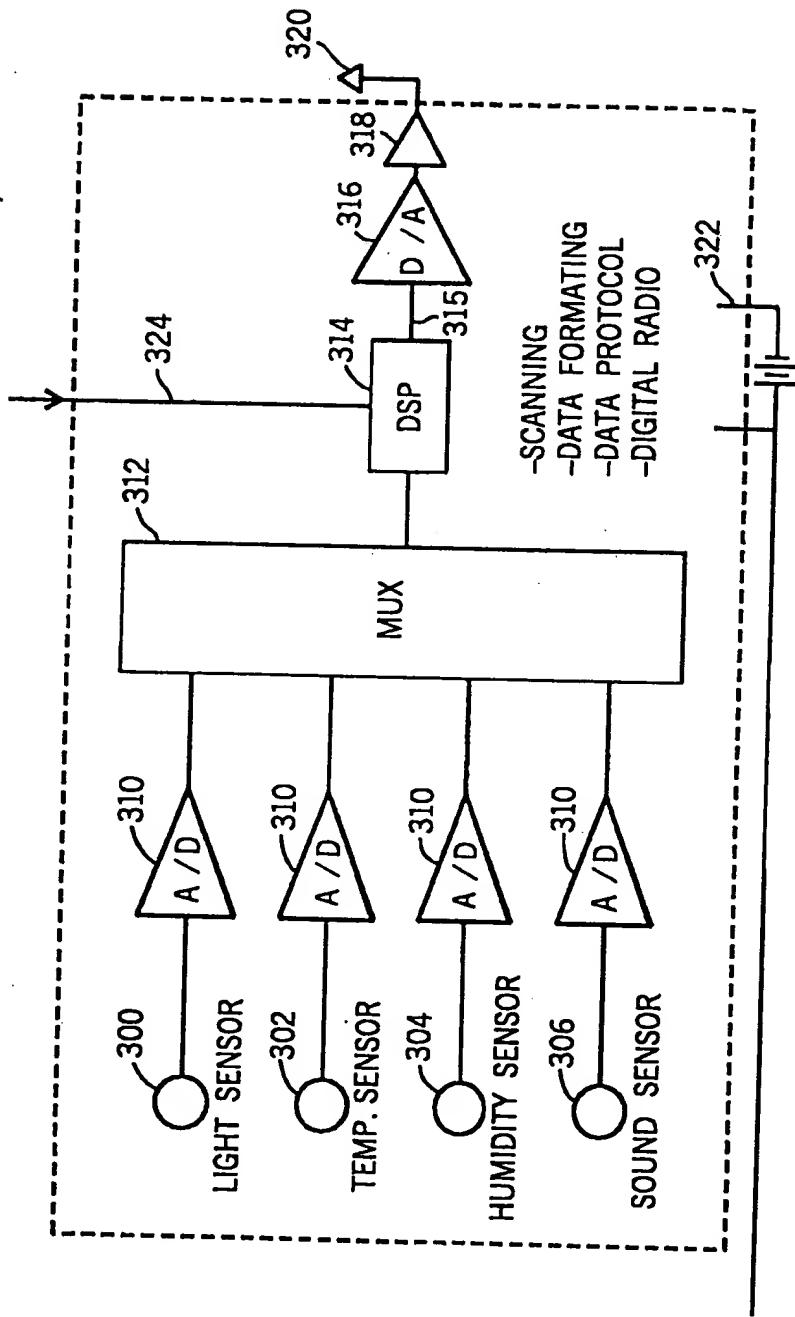


FIG. Z1

